

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	387	refresh and memory and read adj request and write adj request and asynchronously	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 15:42
L2	21	1 and refresh adj timer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 15:42
L3	9	2 and late with control\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 15:43
L4	15197	3 adn refresh adj control adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 15:43
L5	9	3 and refresh adj control adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:13
L6	3	5 and refresh adj request	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:13
L7	3	6 and refresh adj operation with read adj operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:14
L8	0	7 and late adj write adj operation with based with refresh adj request	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:15

L9	0	late adj write adj operation with based with refresh adj request.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:16
L10	1	refresh adj control adj pulse.clm. and refresh adj address adj count-up adj signal.clm. and row adj enable adj normal adj signal. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 16:17